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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q68480

Hidemitsu AOKI, et al.

Appln. No.: 10/071,184

Group Art Unit: 2823

Confirmation No.: 2961

Examiner: Michelle Estrada

Filed: February 11, 2002

For: **MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE**

RESPONSE UNDER 37 C.F.R. § 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated November 18, 2003, please consider the remarks for the above-identified application as follows on the accompanying pages.

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